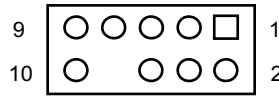
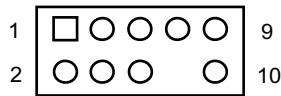


# Intel Front Panel Audio Header Design



**Motherboard Header Pin Layout**



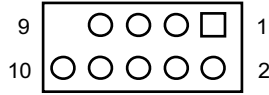
**Block Connector Pin Layout (Cable)**

## Pin Assignments

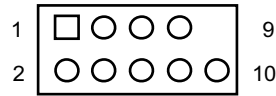
### Front Panel Audio Header Signal Names

Pin	Signal Name	Description
1	AUD_MIC	Front Panel Microphone input signal
2	AUD_GND	Ground used by Analog Audio Circuits
3	AUD_MIC_BIAS	Microphone Power
4	AUD_VCC	Filtered +5 V used by Analog Audio Circuits
5	AUD_FPOUT_R	Right Channel Audio signal to Front Panel
6	AUD_RET_R	Right Channel Audio signal Return from Front Panel
7	HP_ON	RSVD for future use to control Headphone Amplifier
8	KEY	No Pin
9	AUD_FPOUT_L	Left Channel Audio signal to Front Panel
10	AUD_RET_L	Left Channel Audio signal Return from Front Panel

# Intel Front Panel Firewire IEEE-1394 Header Design



**Motherboard Header Pin Layout**



**Block Connector Pin Layout**

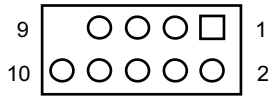
## Pin Assignments

Front Panel IEEE-1394 Connector

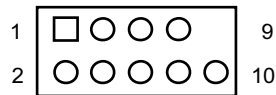
Pin	Signal Name	Pin	Signal Name
1	TPA+	2	TPA-
3	Ground	4	Ground
5	TPB+	6	TPB-
7	+12V (Fused)	8	+12V (Fused)
9	Key (no pin)	10	Ground

Note: IEEE-1394 ports may be assigned as needed.

# Intel Front Panel USB Header Design



**USB Motherboard Header Pin Layout**



**USB Block Connector Pin Layout**

## Pin Assignments

### Front Panel USB Connector

Pin	Signal names	Description
1	VREG_FP_USBPWR0	Front Panel USB Power (Ports 0,1)
2	VREG_FP_USBPWR0	Front Panel USB Power (Ports 0,1)
3	USB_FP_P0-	Front Panel USB Port 0 Negative Signal
4	USB_FP_P1-	Front Panel USB Port 1 Negative Signal
5	USB_FP_P0+	Front Panel USB Port 0 Positive Signal
6	USB_FP_P1+	Front Panel USB Port 1 Positive Signal
7	Ground	
8	Ground	
9	Key (no pin)	
10	USB_FP_OC0	Front Panel USB Overcurrent signal (Ports 0,1)

Note: USB ports may be assigned as needed.